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PATENT DATE

CLASS _327 _7/4 EXAMINER APPLICATION NO. 09/891310 ART UNIT 2816 CONT/PRIOR SUBCLASS 727 Masahito Suzuki APPLICANTS RSO

Integrated circuit device having boundary scan register

TITLE

PTO-2040 12/99

		ISSUING C	CLASSIFICATION					
ORIGIN	AL		CROSS REFERENCE(S)					
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)					
INTERNATIONA	L CLASSIFICATION							
			Continued on Issue Slip Inside File Jacket					

TERMINAL		DRAWINGS		CLAIMS ALLOWED		
DISCLAIMER	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.	
The term of this patent				NOTICE OF ALI	LOWANCE MAILED	
subsequent to (date) has been disclaimed.	(Assistant Examiner) (Date					
The term of this patent shall not extend beyond the expiration date of U.S Patent. No.				ISS	UE FEE	
				Amount Due	Date Paid	
	(Primary	Examiner)	(Date)		_	
The terminalmonths of				ISSUE BATCH NUMBER		
this patent have been disclaimed.	(Legal Instrum	nents Examiner)	(Date)			
WARNING: The information disclosed herein may be re- Possession outside the U.S. Patent & Trader					35, Sections 122, 181 and 36	
orm PTO-436A Rev. 6/99)			FILED WITH	: DISK (CRF)	FICHE CD-RC	

(Rev. 6/99)

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